Variable splitting ratio $2 \times 2$ MMI couplers using multimode waveguide holograms

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Abstract: Variable power splitting ratio $2 \times 2$ MMI couplers using multimode waveguide holograms are analyzed. Theoretical analysis shows that variable splitting ratios can be obtained with surface relief holograms on MMI couplers with fixed dimensions. Devices with paired-imaging lengths are designed on a silicon-on-insulator (SOI) platform. Beam propagation simulations are used to verify a matrix theory analysis and to investigate proposed device performance. Fabrication tolerance of the proposed device is also analyzed.

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References and links
1. Introduction

Power splitters are important building blocks in integrated photonics. Multimode interference (MMI) couplers [1] are particularly attractive for power splitter applications because of their large fabrication tolerance, large bandwidth, and compact size [2]. For traditional 2×2 MMI power splitters, only limited splitting ratios can be achieved using the imaging properties of MMIs [3]. A free choice of power splitting ratio can be achieved with butterfly MMIs [4] or bent MMIs [5] by phase shifts from geometrical variations. However, it could be difficult to cascade these angled or bent devices with other components in integrated optical circuits. Another recently demonstrated approach is to cascade MMIs of different widths to achieve new power splitting ratios [6]. This approach relies on changing the device dimension and geometry to obtain the new splitting ratios. All these approaches require different device dimensions or geometries for different splitting ratios, which could result in potential design difficulties when new splitting ratios are required in existing layouts. A power splitter with fixed device geometry for various splitting ratios is highly desirable in terms of compatibility and ease of integration.

In a recent work [7], it was demonstrated that unitary transformations can be implemented with multimode waveguide holograms. By etching shallow surface relief patterns on the multimode section of an MMI, arbitrary unitary transformations can be implemented with holograms without changing its geometry. In this work, we extend the concept of hologram design by eigenmodes at self-imaging planes [7] to include eigenmodes at paired-imaging planes for the hologram calculation. Using a matrix theory, 2×2 couplers with variable power splitting ratios are designed on a silicon-on-insulator (SOI) [8] platform for its compatibility and ease of integration with the silicon CMOS technology. Beam propagation modeling is used to verify the design concept and investigate the performance of designed devices.

2. Matrix theory of hologram in MMI power splitters at paired-imaging length

Figure 1 shows the schematic of 2×2 MMI power splitters considered in this work. The length $L$ of the multimode section is at half the beat length, $L_\pi$ [1]:

$$L_\pi = \frac{4n_cW_e^2}{3\lambda},$$

(1)

where $n_c$ is the core effective index, $W_e$ is the effective width [1] of the multimode section, and $\lambda$ is the wavelength. By placing the access waveguides at ±$W_e/6$ from the center of the multimode section, selected modes are excited to produce a paired-image at the 3dB length of $L_\pi/2$ [1]. For the electric field in a single input waveguide, a paired image of equal amplitude and a phase difference of $\pi/2$ is formed at the output bar and cross waveguides. Without a hologram, the MMI functions as a power splitter with 50:50 splitting ratio.

The hologram is in the form of surface relief patterns on top of the multimode section to provide effective index modulation $\Delta n$ at different parts of the multimode section. Magnitude of $\Delta n$ can be varied by changing the depth of the surface relief patterns [7]. It was pointed out in Ref.[7] that eigenmodes at self-imaging planes [9] can be used to calculate holograms for unitary matrix-vector multiplications. The interference pattern between the $i$th and the $j$th eigenmodes corresponds to the $ij$th element in matrix $K$, which relates input vector $V_i$ and output vector $V_o$ through unitary matrix-vector multiplication as

$$V_o = \exp(-iKL_H)V_i,$$

(2)
where $L_H$ is the length of the hologram.

Here, we further extend the theory by using eigenmodes at paired-imaging planes for hologram calculation. At the power splitter input, two eigenvectors $e_a = [1, 0]^T$ and $e_b = [0, 1]^T$ can be used to represent the complex electric field amplitudes in the input waveguides. When there is no hologram, the transfer matrix $T$ representing the MMI is

$$T = \frac{1}{\sqrt{2}} \begin{bmatrix} 1 & i \\ i & 1 \end{bmatrix}.$$  \hfill (3)

At the paired imaging plane, we define two new eigenvectors $E_a = \left(\frac{1}{2}\right)\left[\frac{1}{\sqrt{2}}, i\right]^T$ and $E_b = \left(\frac{1}{2}\right)\left[i, \frac{1}{\sqrt{2}}\right]^T$ to represent the complex electric field amplitudes in the output waveguides. As a result, the MMI represents an identity matrix mapping the input eigenvectors $e_a, e_b$ to the output eigenvectors $E_a, E_b$. Vectors spanned by $E_{a,b}$ are related to vectors spanned by $e_{a,b}$ via basis rotation as

$$V_e = TV_E.$$  \hfill (4)

We design the hologram by calculating the interference pattern of the two input eigenvectors in the multimode waveguide. The hologram loaded MMI can be represented in matrix form relating input and output vectors as

$$V_E(L) = \exp(-iKL_H)V_e(0) = HV_e(0)$$  \hfill (5)

(note that different bases are used for input and output vectors). $K$ is a Hermitian matrix representing the hologram as

$$K = \begin{bmatrix} 0 & \kappa \\ \kappa & 0 \end{bmatrix}.$$  \hfill (6)

where $\kappa$ is a real number representing the coupling coefficient, its magnitude is on the order of $(2\pi/\lambda)(\Delta n/n_c)$ \cite{7}, and a common phase factor on the diagonal is ignored for clarity. Using Eq.(4)-Eq.(6), the output field amplitudes in both output ports are thus related to input ports by

$$V_e(L) = THV_e(0) = \frac{1}{\sqrt{2}} \begin{bmatrix} \cos \kappa L_H + \sin \kappa L_H & i(\cos \kappa L_H - \sin \kappa L_H) \\ i(\cos \kappa L_H - \sin \kappa L_H) & \cos \kappa L_H + \sin \kappa L_H \end{bmatrix} V_e(0).$$  \hfill (7)

For a single input port, the output power at bar and cross ports is thus

$$\text{bar: } \frac{1}{2}(1 + \sin 2\kappa L_H)$$  \hfill (8)
cross: $\frac{1}{2}(1 - \sin 2\kappa L_H)$.

From Eq.(8) and Eq.(9), it is obvious that an arbitrary power splitting ratio can be obtained by varying $\kappa$ via index modulation $\Delta n$ or by varying hologram length $L_H$, while maintaining the dimensions of the MMI.

3. Device design

In this study, an SOI rib waveguide structure [10] with larger mode cross section as compared to the nanowire structure [11] is chosen for its lower coupling loss to optical fibers. The proposed design parameters for the SOI-based MMI in Fig. 1 are chosen as follows: the waveguide height $H$ is 3$\mu$m, the slab height $h$ is 2$\mu$m, the buried oxide layer thickness $d$ is 1$\mu$m, the width of access waveguides $W_a$ is 4$\mu$m, the width of the multimode section $W_{mmi}$ is 24$\mu$m, the length $L$ of the multimode section is 1080$\mu$m, the centers of access waveguides are placed $\pm$4.3$\mu$m from the center of the multimode section for paired-imaging. The placement of access waveguides is chosen by approximating the effective width $W_e$ of MMI by taking into account the lateral penetration of the fundamental mode into the cladding as in Ref. [1]. Refractive indices are $n_a$=1.0(air), $n_{si}$=3.6, and $n_{ox}$=1.46. The device is optimized for 1550nm input wavelength and TE polarization.

A 2D wide-angle beam propagation method (WA-BPM) [12] based model is used for device design and simulation. The 3D waveguide structure is converted to a 2D structure using the effective index method [13]. The imaging properties of MMI structures are based on the quadratic dependence between the mode effective indices and the mode numbers. To validate the effective index calculation, the calculated mode effective indices are compared with effective indices calculated with a full vectorial 3D mode solver [14]. The results show a good agreement between the 2D approximation and the full vectorial solution. We expect the 2D WA-BPM calculation to be a very good approximation of the 3D structure.

The hologram is calculated using WA-BPM by propagating equal amplitude and in-phase inputs in the MMI as shown in Fig. 2. In device fabrication, the calculated pattern can be converted to a binary mask and transferred to the surface of the multimode section [7]; the etching depth $d_{etch}$ of the surface relief hologram pattern is directly related to the magnitude of effective index modulation $\Delta n$ and can be estimated by the effective index method [13]. For this particular design, we calculated the effective index $n$ of the multimode section as a function of the waveguide height $h$ using the effective index method and found $\Delta n$ and $d_{etch}$ (the variation in waveguide height, $\Delta h$) follow an almost linear relation with a slope of $\Delta n/d_{etch} = -7 \times 10^{-6}/$nm. By varying $d_{etch}$, the splitting ratio can be varied through coupling coefficient $\kappa$ as shown in Eq.(8) and (9). For a fixed $d_{etch}$, the splitting ratio can be varied by changing the length of the hologram $L_H$ on the multimode section.

4. Device simulation

WA-BPM simulations are used to verify the matrix theory analysis in section 2 and to investigate the performance of the proposed device. In the simulations presented below, the interference pattern in the multimode section in Fig. 2 is used as a perturbation to the core effective index. The hologram features are discretized into $1\mu m \times 1\mu m$ pixels in the simulation.

4.1. Variable power splitting ratio

The dependence of the device power splitting ratio on the coupling coefficient $\kappa$ is investigated by varying the index modulation $\Delta n$ using an input wavelength of 1550nm. The normalized output power in the bar and cross ports are plotted as a function of index modulation in Fig. 3. The
two curves in the same figure are theoretical fits with Eq.(8) and Eq.(9). Excellent agreement between the matrix theory and WA-BPM simulations can be seen in the figure. An effective index modulation of -0.002 is required for a full swing of the power splitting ratio at the output ports. Using the effective index method, it is found that a $\Delta n$ of -0.002 can be obtained by surface relief patterns etched into the multimode section of the MMI with a depth of approximately 300nm.

4.2. Wavelength dependence of MMI power splitter with hologram

We define $R$ as the ratio between bar port output intensity and cross port output intensity. Using power splitters designed and optimized for operation at 1550nm, we vary the input wavelengths from 1500nm to 1600nm in increments of 10nm to investigate wavelength dependence of these devices. We define the difference between $R$ and the designed value at 1550nm, $R_{1550}$, as $\Delta R$. These variations for a 50:50 MMI splitter without holograms, along with three hologram-loaded MMIs with index modulations of -0.0005,-0.0010,-0.0015 are normalized against the designed value $R_{1550}$ and shown in Fig. 4. It can be seen that the spectral variations in the power splitting ratios of hologram-loaded MMIs are comparable to that of the MMI splitter without holograms. We also plot the normalized total transmittance of these devices as a function of wavelength in Fig. 5. Transmittance decrease as wavelength is detuned from the design wavelength. As
expected, excess loss is observed when index modulation is increased. Overall, the devices exhibit excellent power splitting performance and high transmittance over a wide bandwidth of 100nm.

5. Fabrication tolerance

The fabrication tolerance of these devices can be addressed separately for the hologram and the MMI. Errors in the hologram etching depth, $d_{\text{etch}}$, is directly related to errors in the effective index modulation, $\Delta n$, which affects the power splitting ratio as shown in Fig. 3. For our design example, since $d_{\text{etch}}$ and $\Delta n$ follow a linear relation, the sensitivity of power splitting ratio to $d_{\text{etch}}$ is thus the slope of the curves in Fig. 3. We consider a $d_{\text{etch}}$ error of 30nm (corresponding $\Delta n$ error of 0.0002) and calculate its effect on the power splitting ratio. The maximum error is found for the 50:50 splitter (maximum slope) with a resulting splitting ratio of 57.8:42.2; while
the minimum error is found for the 100:0 splitter (minimum slope) with a resulting splitting ratio of 99.4:0.6. For fabrication errors in the MMI, such as the width, $W_{\text{mmi}}$, due to undercutting from the etching process, the optimal operating wavelength will be changed for the designed MMI length as can be seen in Eq. (1). In this case, the hologram design is no longer optimal at the original designed wavelength. The hologram basically consists of multiplexed long-period gratings with grating periods defined by the differences between the propagation constants of the modes of the multimode section [7]. From Eq. (18) in [1], we know that the difference in propagation constants between two modes $j$ and $k$ can be expressed as

$$k_{jk} = \frac{\pi \lambda}{4n_c W_c^2} (j^2 - k^2).$$  \hspace{1cm} (10)

As a result, as long as the condition in Eq. (1) is satisfied at the new optimal wavelength, the grating period defined in Eq. (10) will be satisfied as well. In other words, the designed hologram is now optimal at a new wavelength as a result of the MMI fabrication error. We expect the device to show a similar spectral dependence to the ones shown in Figs. 4 and 5 but centered at the new optimum wavelength.

6. Summary

We have demonstrated the feasibility of generating arbitrary power splitting ratios with multimode waveguide holograms on $2 \times 2$ MMI couplers. The principle of hologram design using self-imaging properties is extended for the case of paired-interference and can be applied to any intermediate imaging planes. The same principle can be used to design $N \times N$ power splitters or interconnects with new splitting ratios.

Based on the new design principle, variable $2 \times 2$ power splitters are designed using SOI rib waveguide structures. These devices should have good performance over a bandwidth of 100nm as shown by beam propagation simulations.

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